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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/874,437	06/05/2001	Yukio Sato	33670	5800

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EXAMINER

PHU, SANH D

ART UNIT

PAPER NUMBER

2682

DATE MAILED: 09/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/874,437	SATO, YUKIO	
	Examiner	Art Unit	
	Sanh D. Phu	2682	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 July 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 3,5,7,9,10 and 12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 13-18 is/are allowed.
- 6) ☒ Claim(s) 3,5,7,9,10,12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

This Office Action is responsive to the Amendment filed on 7/1/05.

Claim Rejections – 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this

Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 3, 7–10 are rejected under 35 U.S.C. 102(e) as being anticipated by Kanbara et al (6,516,201), previously cited.

–Regarding to claim 3, see fig.1, col. 3, line 15 to col. 5, line 6, Kanbara et al disclose a multi-mode cellular phone terminal comprising:

radio communications means (2, 3, 4, 5) connected to an antenna for transmitting (TX)/receiving (RX) radio waves (see Fig. 1, col. 3, line 22 to col. 4, line 33);

signal processing means (6, 7, 8) for transmitting/receiving a transmit/receive signal to/from said radio communications means (see Fig. 1, col. 3, line 22 to col. 4, line 33); and

communications control means (40, 41, 42) for controlling said radio communications means and said signal processing means, said multi-mode cellular phone terminal supporting a plurality of communications systems (see Fig. 1, col. 3, line 22 to col. 4, line 33),

wherein said radio communications means is composed of hardware to be used in common by a plurality of communications systems (6, 7, 8, 40), and said signal processing means is composed of hardware to execute signal processing supporting a plurality of communications systems (voice communication system (9, 10, 11, 12) and data communication system (20, 50, PC)) (see Fig. 1, col. 3, line 22 to col. 4, line 33),

wherein said signal processing means can support different bit rates and modulation systems (CDMA, PCM) (see figure 1, and col. 4, lines 1–21) using different communications system timings in such a way that said processing means comprises CDMA SIGNAL PROCESSING SECTION (6), (which inherently includes a communication system timing circuit of providing chip rate (data rate) of a PN code for CDMA modulation/demodulation process carried out in the CDMA SIGNAL PROCESSING SECTION (6)), and VOICECODE PROCESSING SECTION (7) and PCM CODE PROCESSING SECTION (8), (wherein the VOICECODE PROCESSING SECTION (7) receives and utilize a communications system timing of “reception data rate” provided from controller means (40) of said communications control means (see col. 3, lines 45–47), and wherein the VOICECODE PROCESSING SECTION (7) and PCM CODE PROCESSING SECTION (8) receive and utilize different communications system timings of “transmission data rates” provided from means (40) of said communications control means for voice communication or data communication (see col. 4, lines 1–16));

wherein communication control means, as being explained above, therefore, can support different communication system timings (timings of “reception data rate” and “transmission data rates”).

-Regarding to claim 7, Kanbara et al disclose that a multi-mode cellular phone terminal wherein said signal processing means (6,7,8) executes modulation/demodulation supporting a plurality of communications systems and has a signal processor composed of common hardware and memory storing a plurality of signal processing programs (see Fig. 1, col. 3, line 22 to col. 4, line 33).

-Regarding to claim 9, Kanbara et al disclose that a multi-mode cellular phone terminal wherein said signal processing means has a signal processor composed of common hardware and read/write memory storing the minimum signal processing programs to support each communications system (see Fig.1, col. 4, line 38 to col. 5, line 6).

Regarding to claim 10, Kanbara et al disclose that a multi-mode cellular phone terminal wherein said communications control means has a controller (40) supporting a plurality of communications systems and memory storing

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control programs supporting the multi-mode (see Fig.1, col. 4, line 38 to col. 5, line 6).

Claim Rejections – 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 5 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kanbara et al in view of Ohbichi et al (5,585,617).

–Regarding to claim 5, Kanbara et al does not disclose a clock having a frequency necessary for modulation/demodulation at a plurality of different data rate is generated by frequency division means via different dividing number or fractional frequency division of a reference clock output from a single oscillator.

Ohbichi discloses a clock generator (12, 13, 14) for generating a clock with desired frequency rates wherein the clock is generated by frequency division means (14) making integral frequency division via different dividing number or fractional frequency division of a common reference clock output from a single oscillator (13) (see figure 2, col. 4, lines 56–65).

Since in Kanbara et al, controller means (40) of said communications control means provides different bit rates “transmission data rates” (see col. 4, lines 1–16) and Kanbara et al does not discloses in detail how said different bit rates are generated. Therefore, for an application, it would have been obvious for a person skilled in the art, when building or carrying out Kanbara et al invention, to implement said controller means (40) with a clock generator for generating a clock with desired frequency rates for providing said different bit rates, as required, wherein the clock is generated by frequency division means making integral frequency division via different dividing number or fractional frequency division of a common reference clock output from a single oscillator, as taught by Ohbichi.

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Regarding to claim 12, Kanbara et al, in view of Ohbichi, discloses a system timer (Swa1, ..., Swan) for switching over a plurality of clocks generated by said frequency division means and counting different timings for generating the clock at a desired frequency in a capability of supporting said plurality of communication systems (see Ohbichi, figure 2).

Allowable Subject Matter

5. Claims 13–18 are allowed.

Response to Arguments

6. Applicant's arguments filed on 7/1/05 have been fully considered but they are not persuasive.

The applicant mainly argues that (i) with respect to claim 3, Kanbara et al does not disclose the limitation “said signal processing can support different bit rates and modulation systems using different communication system timings, and wherein communication control means can support different communication system timings”; and (ii) with respect to claim 12, neither Kanbara et al and Ohbichi teaches “a timer system for switching over a plurality

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clocks generated by said frequency division means and counting different timings to support a plurality of communication systems”.

Regarding to part (i), the examiner respective disagrees. As being explained above in the rejection to claim 3, Kanbara et al teaches that said signal processing means (6, 7, 8) can support different bit rates and modulation systems (CDMA, PCM) (see figure 1, and col. 4, lines 1–21) using different communications system timings in such a way that said processing means comprises CDMA SIGNAL PROCESSING SECTION (6), (which inherently includes a communication system timing circuit of providing chip rate (data rate) of a PN code for CDMA modulation/demodulation process carried out in the CDMA SIGNAL PROCESSING SECTION (6)), and said processing means also comprises VOICECODE PROCESSING SECTION (7) and PCM CODE PROCESSING SECTION (8), (wherein the VOICECODE PROCESSING SECTION (7) receives and utilize a communications system timing of “reception data rate” provided from controller means (40) of said communications control means (40, 41, 42) (see col. 3, lines 45–47), and wherein the VOICECODE PROCESSING SECTION (7) and PCM CODE PROCESSING SECTION (8) receive and utilize different

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communications system timings of “transmission data rates” provided from means (40) of said communications control means for voice communication or data communication (see col. 4, lines 1–16)). And further, in Kanbara et al, said communication control means, as being explained above, therefore, can support different communication system timings (timings of “reception data rate” and “transmission data rates”).

–Regarding to claim (ii), the examiner also disagrees. Kanbara et al, in view of Ohbichi, discloses a system timer (Swa1, ..., Swan) for switching over a plurality of clocks generated by said frequency division means and counting different timings (by clocking for generating the clock at a desired frequency) in a capability of supporting said plurality of communication systems (see Ohbichi, figure 2).

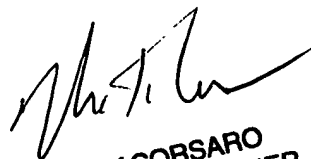
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sanh D. Phu whose telephone number is (571)272–7857. The examiner can normally be reached on 8:00–16:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro can be reached on (571)272-7876. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



NICK CORSARO
PRIMARY EXAMINER

Sanh D. Phu
Examiner
Art Unit 2682

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